

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Tetsuya SHIMOMURA et al.
Application No.: Not yet assigned
(Continuation of 09/991,705, filed November 26, 2001)
Filing Date: Even date herewith
Title: **DATA PROCESSOR HAVING UNIFIED MEMORY
ARCHITECTURE USING REGISTER TO OPTIMIZE MEMORY
ACCESS (AS AMENDED)**
Art Unit: 2671
Examiner: Ulka J. Chauhan

CLAIM FOR PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 25, 2003

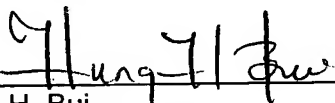
Sir:

Pursuant to 35 U.S.C. §119 and 37 CFR 1.55, the applicants hereby claim the right of priority based on the following foreign applications filed in Japan:

Application No. 8-258725 filed on September 30, 1996; and
Application No. 9-13732 filed on January 28, 1997.

Certified copies of these foreign applications were filed on September 29, 1997, in application Serial No. 08/942,689, the grandparent application of the present continuation application.

Respectfully submitted,
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